

## LTC3787 Supply Design Summary Report

***Vin : 9V (min.), 12V (nom.), 15V (max.)***

***Output Rails : Vout1 = 37,28V / 30A (max.)***

*Project Name : TPA3251 Boost Converter*

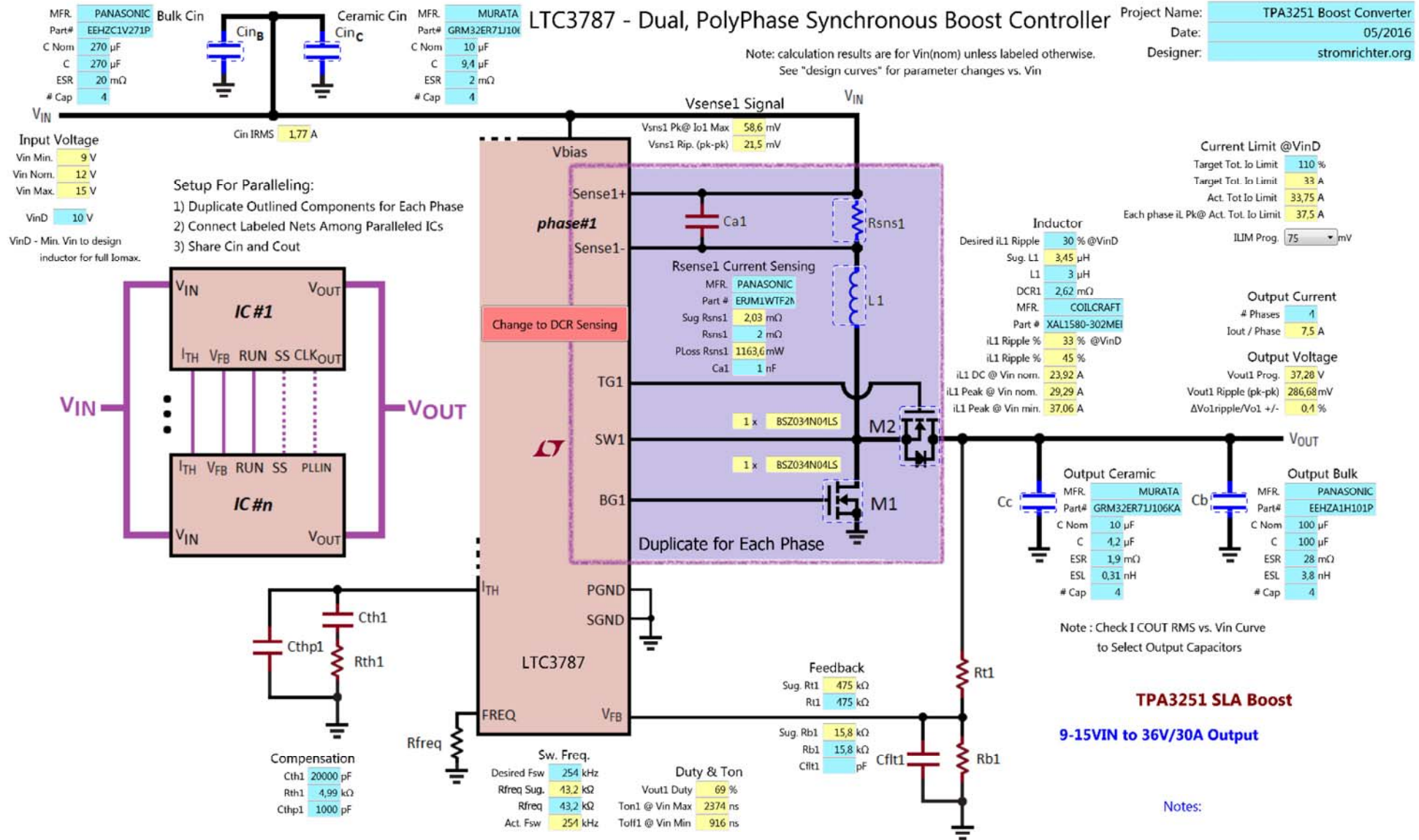
*Project Date : 05/2016*

*Designer : stromrichter.org*

# LTC3787 Solution - Simplified Schematic

Vin : 9V (min.), 12V (nom.), 15V (max.)

Output Rails : Vout1 = 37,28V / 30A (max.)

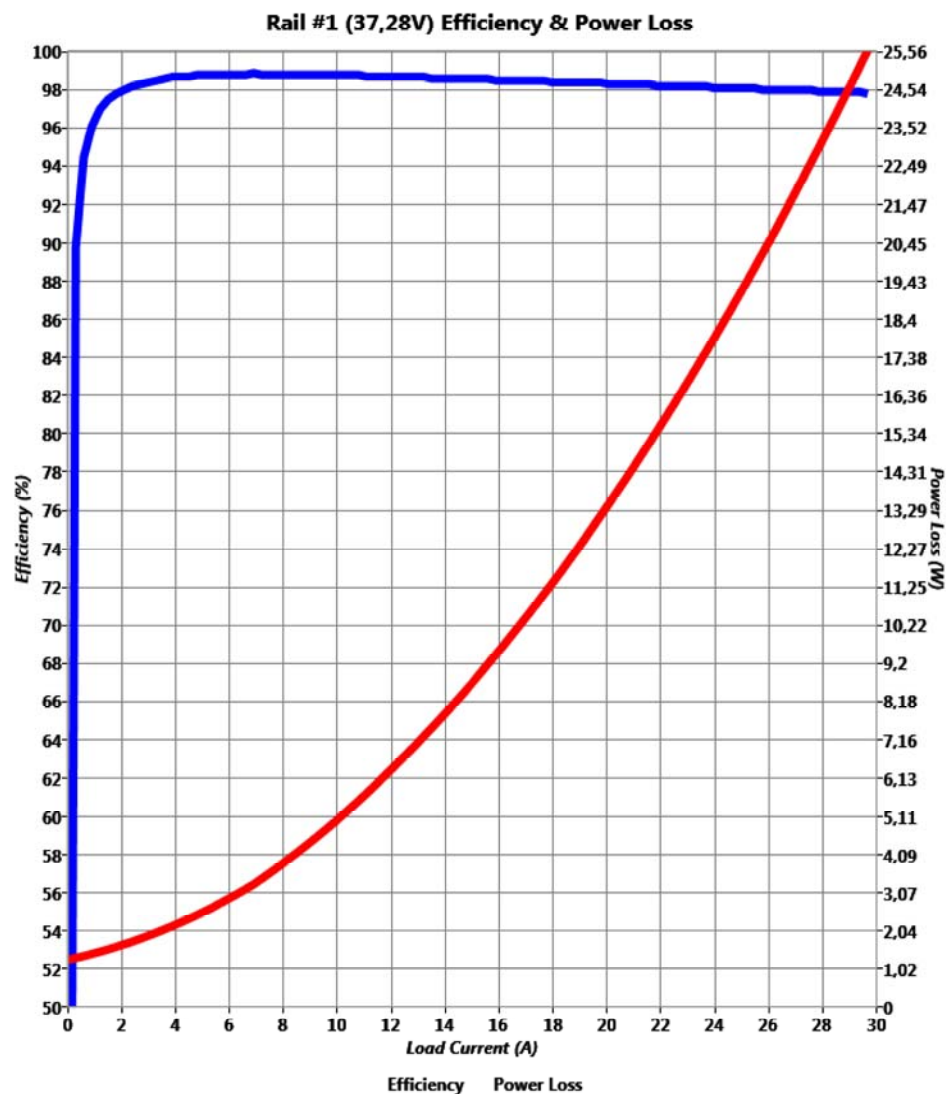


Linear Technology Confidential - For Internal Use Only

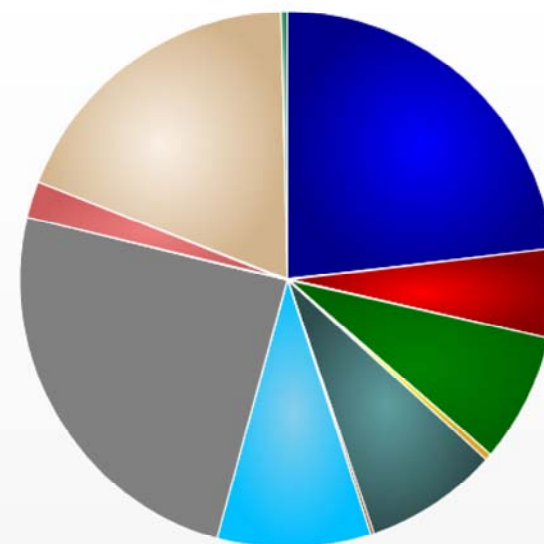
# LTC3787 Solution - Efficiency & Loss Estimations

Rail # 1 :  $V_{in} = 10V$ ,  $V_{out1} = 37,28V$

\* Estimations For CCM Mode Only. Inductor AC Losses Entered by User



**Rail #1 (37,28V) Power Loss Breakdown (Full Load)**



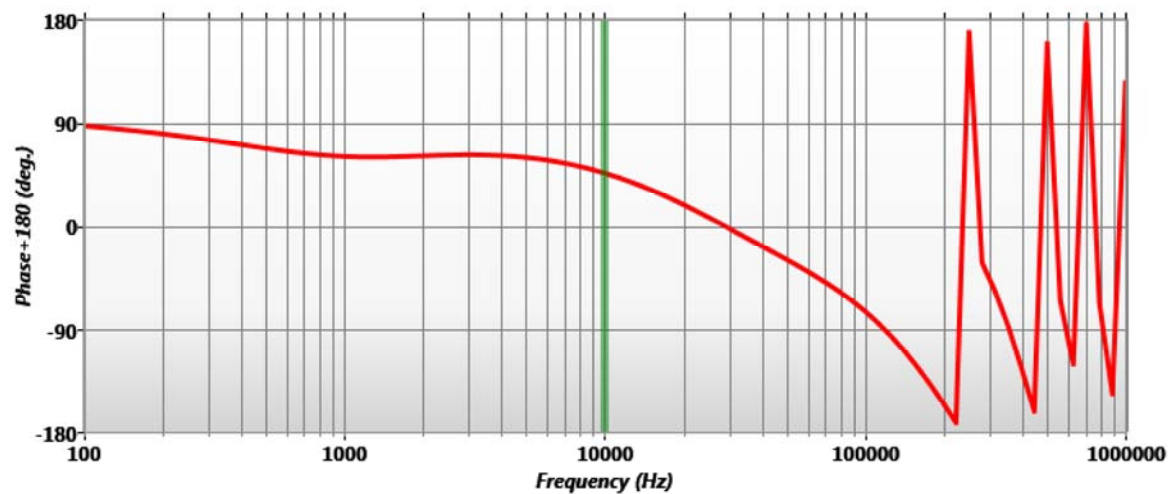
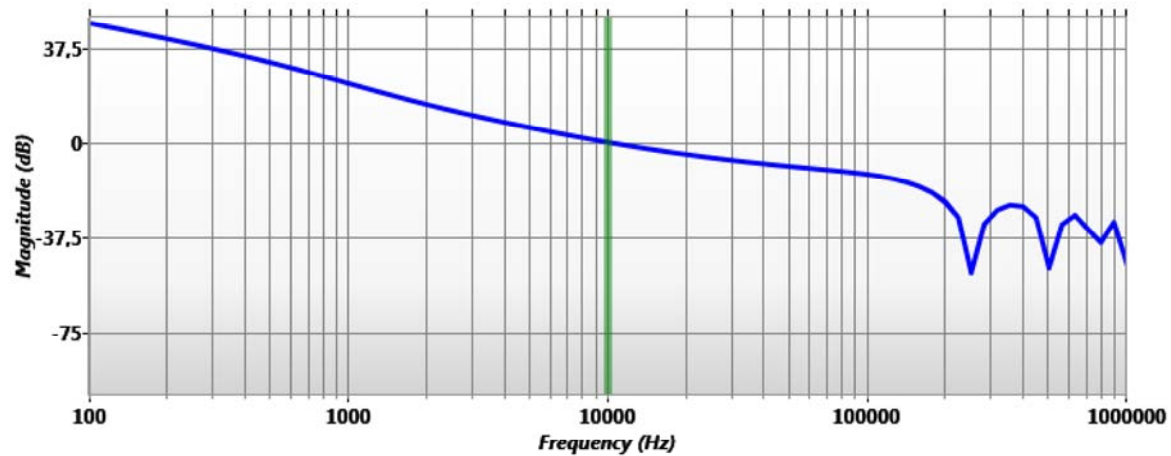
- Control Fet Conduction (8,437W,23,15%)
- Control Fet Turn On (1,95W,5,35%)
- Control Fet Turn Off (2,847W,7,81%)
- IC LDO (0,143W,0,39%)
- Sync Fet Conduction (2,937W,8,06%)
- Sync Fet Driving (0,072W,0,2%)
- Sync Fet Body Diode (3,388W,9,3%)
- Inductor DCR (8,922W,24,48%)
- Inductor Core (0,8W,2,19%)
- Rsense (6,811W,18,69%)
- Cout (0,14W,0,38%)

## LTC3787 Solution - Loop Gain & Load Transient Estimations

Rail # 1 :  $V_{in} = 10V$ ,  $V_{out1} = 37,28V$ ,  $I_{out1} = 25A$

*\* Estimations For CCM Mode Only. Estimations Based On Small Signal Avg. Model*

**Rail #1 (37,28V) Loop Gain**



# LTC3787 Solution - Summary

## LTC3787 Supply Design Summary



Project Info: TPA3251 Boost Converter, 05/2016, stromrichter.org

### Design Specifications

#### Steady State :

Rail #	Vin Min.	Vin Nom.	Vin Max.	Fsw	Vo	$\Delta V_o$ rip. p-p	$\Delta V_o$ rip. %	Io Max	$\Delta I_L$ p-p	$\Delta I_L$ %	I <sub>Lpk</sub>	Duty Max	Ton min.	Toff min.
1	9 V	12 V	15 V	254 kHz	37,28 V	286,68 mV	0,4 %	30 A	10,74 A	45 %	29,29 A	7697 %	2374 ns	916 ns

#### Efficiency and Loop :

Rail #	Vo	I <sub>o</sub> max	Eff.@I <sub>o</sub> max	P <sub>Loss</sub> @I <sub>o</sub> max	Loop BW	Loop PM
1	37,28 V	30 A	96,89 %	35,846 W	10 kHz	45,29 deg

#### Recommendations and Warnings :

Message
---------

### Power Components

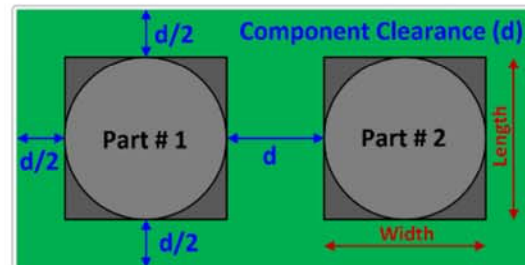
#### Power Components Bill Of Materials :

Export BOM

Ref. Des.	Value	Quantity	Description	Mfr. Name	Mfr. Part #	Pkg. (Imperial)	L(mm)	W(mm)	H(mm)	User Note
U1 U2		2	IC	LINEAR TECH	LTC3787		5	4	0.8	
Lo1 Lo2 Lo3 Lo4	3 $\mu$ H	4	IND	COILCRAFT	XAL1580-302ME8		16.4	15.4	7.5	
Cinb1 Cinb2 Cinb3 Cinb4	270 $\mu$ F	4	CAP	PANASONIC	EEHZC1V271P	G	10	10	10.2	
Cinc1 Cinc2 Cinc3 Cinc4	10 $\mu$ F	4	CAP	MURATA	GRM32ER71J106KA12	1210	3.2	2.5	2.7	
Cob1 Cob2 Cob3 Cob4	100 $\mu$ F	4	CAP	PANASONIC	EEHZA1H101P	G	10	10	10.2	
Coc1 Coc2 Coc3 Coc4	10 $\mu$ F	4	CAP	MURATA	GRM32ER71J106KA12	1210	3.2	2.5	2.7	
Rsense1 Rsense2 Rsense3 Rsense4	2m $\Omega$	4	RES	PANASONIC	ERJM1WTF2M0	2512	6.4	3.2	0.8	
Mctrl1 Mctrl2 Mctrl3 Mctrl4	40V	4	FET	Infineon	BSZ034N04LS		3.4	3.4	1.1	
Msync1 Msync2 Msync3 Msync4	40V	4	FET	Infineon	BSZ034N04LS		3.4	3.4	1.1	

#### Power Components Footprint :

# Components	34
Max. Height	10,2 mm
Component Clearance (d)	1 mm
* Power Components Area (Excludes ICs)	2506,2 mm <sup>2</sup> 3,885 in <sup>2</sup>
* Power Components Area (Includes ICs)	2566,2 mm <sup>2</sup> 3,978 in <sup>2</sup>



#### \* Note :

The calculated power component area is only the simple sum of component footprint areas with given clearance, assuming all power components are on the same side of PCB. It is NOT the final PCB size with layout design.